## RD74LVC374B

Octal D-type Flip Flops with 3-state Outputs
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## Description

The RD74LVC374B has eight edge trigger D type flip flops with three state outputs in a 20 pin package. Data at the D inputs meeting set up requirements are transferred to the Q outputs on positive going transitions of the clock input. When the clock input goes low, data at the D inputs will be retained at the outputs until clock input returns high again. When a high logic level is applied to the output control input, all outputs go to a high impedance state, regardless of what signals are present at the other inputs and the state of the storage elements. Low voltage and high-speed operation is suitable at the battery drive product (note type personal computer) and low power consumption extends the life of a battery for long time operation.

## Features

- $\mathrm{V}_{\mathrm{CC}}=1.65 \mathrm{~V}$ to 5.5 V
- All inputs $\mathrm{V}_{\mathrm{IH}}$ (Max. $)=5.5 \mathrm{~V}\left(@ \mathrm{~V}_{\mathrm{CC}}=0 \mathrm{~V}\right.$ to 5.5 V$)$
- All outputs $\mathrm{V}_{\text {OUt }}($ Max. $)=5.5 \mathrm{~V}\left(@ \mathrm{~V}_{\mathrm{CC}}=0 \mathrm{~V}\right.$ or output off state $)$
- Typical $\mathrm{V}_{\mathrm{OL}}$ ground bounce $<0.8 \mathrm{~V}\left(@ \mathrm{~V}_{\mathrm{CC}}=3.3 \mathrm{~V}, \mathrm{Ta}=25^{\circ} \mathrm{C}\right)$
- Typical $\mathrm{V}_{\mathrm{OH}}$ undershoot $>2.0 \mathrm{~V}\left(@ \mathrm{~V}_{\mathrm{CC}}=3.3 \mathrm{~V}, \mathrm{Ta}=25^{\circ} \mathrm{C}\right)$
- High output current $\pm 4 \mathrm{~mA}\left(@ \mathrm{~V}_{\mathrm{CC}}=1.65 \mathrm{~V}\right)$
$\pm 8 \mathrm{~mA}\left(@ \mathrm{~V}_{\mathrm{CC}}=2.3 \mathrm{~V}\right)$
$\pm 12 \mathrm{~mA}\left(@ \mathrm{~V}_{\mathrm{CC}}=2.7 \mathrm{~V}\right)$
$\pm 24 \mathrm{~mA}\left(@ \mathrm{~V}_{\mathrm{CC}}=3.0 \mathrm{~V}\right.$ to 5.5 V$)$
- Ordering Information

| Part Name | Package Type | Package Code | Package <br> Abbreviation | Taping Abbreviation <br> (Quantity) |
| :--- | :--- | :--- | :--- | :--- |
| RD74LVC374BFPEL | SOP-20 pin (JEITA) | FP-20DAV | FP | EL (2,000 pcs/reel) |
| RD74LVC374BTELL | TSSOP-20 pin | TTP-20DAV | T | ELL $(2,000 \mathrm{pcs} / \mathrm{reel})$ |

## Function Table



Pin Arrangement


## Absolute Maximum Ratings

| Item | Symbol | Ratings | Unit | Conditions |
| :---: | :---: | :---: | :---: | :---: |
| Supply voltage | $\mathrm{V}_{\mathrm{CC}}$ | -0.5 to 7.0 | V |  |
| Input diode current | $\mathrm{I}_{1}$ | -50 | mA | $\mathrm{V}_{1}=-0.5 \mathrm{~V}$ |
| Input voltage | V | -0.5 to 7.0 | V |  |
| Output diode current | lok | -50 | mA | $\mathrm{V}_{\mathrm{O}}=-0.5 \mathrm{~V}$ |
|  |  | 50 |  | $\mathrm{V}_{\mathrm{O}}=\mathrm{V}_{\text {cc }}+0.5 \mathrm{~V}$ |
| Output voltage | $\mathrm{V}_{0}$ | -0.5 to $\mathrm{V}_{\mathrm{CC}}+0.5$ | V | Output "H" or "L" |
|  |  | -0.5 to 7.0 |  | Output "Z" or $\mathrm{V}_{\mathrm{cc}}$ :OFF |
| Output current | 10 | $\pm 50$ | mA |  |
| $\mathrm{V}_{\mathrm{CC}}$, GND current / pin | $\mathrm{ICC}^{\text {or }} \mathrm{I}_{\text {GND }}$ | 100 | mA |  |
| Storage temperature | Tstg | -65 to +150 | ${ }^{\circ} \mathrm{C}$ |  |

Note: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

Recommended Operating Conditions

| Item | Symbol | Ratings | Unit | Conditions |
| :---: | :---: | :---: | :---: | :---: |
| Supply voltage | $\mathrm{V}_{\text {cc }}$ | 1.5 to 5.5 | V | Data hold |
|  |  | 1.65 to 5.5 |  | At operation |
| Input / output voltage | $\mathrm{V}_{1}$ | 0 to 5.5 | V | G, CK, D |
|  | $\mathrm{V}_{\text {O }}$ | 0 to V CC |  | Output "H" or "L" |
|  |  | 0 to 5.5 |  | Output "Z" or $\mathrm{V}_{\text {cc }}$ :OFF |
| Operating temperature | Ta | -40 to 85 | ${ }^{\circ} \mathrm{C}$ |  |
| Output current | IOH | -4 | mA | $\mathrm{V}_{\mathrm{CC}}=1.65 \mathrm{~V}$ |
|  |  | -8 |  | $\mathrm{V}_{C \mathrm{CC}}=2.3 \mathrm{~V}$ |
|  |  | -12 |  | $\mathrm{V}_{\mathrm{CC}}=2.7 \mathrm{~V}$ |
|  |  | -24 |  | $\mathrm{V}_{\mathrm{CC}}=3.0 \mathrm{~V}$ to 5.5 V |
|  | loL | 4 | mA | $\mathrm{V}_{\mathrm{CC}}=1.65 \mathrm{~V}$ |
|  |  | 8 |  | $\mathrm{V}_{\mathrm{CC}}=2.3 \mathrm{~V}$ |
|  |  | 12 |  | $\mathrm{V}_{\mathrm{CC}}=2.7 \mathrm{~V}$ |
|  |  | 24 |  | $\mathrm{V}_{C C}=3.0 \mathrm{~V}$ to 5.5 V |
| Input rise / fall time*1 | $\mathrm{tr}_{\mathrm{r}} \mathrm{t}_{\mathrm{f}}$ | 20 | ns/V | $\mathrm{V}_{\mathrm{CC}}=1.65 \mathrm{~V}$ to 2.7 V |
|  |  | 10 |  | $\mathrm{V}_{\mathrm{CC}}=3.0 \mathrm{~V}$ to 5.5 V |

Notes: 1. This item guarantees maximum limit when one input switches.
Waveform: Refer to test circuit of switching characteristics.

## Electrical Characteristics

| Item | Symbol | Vcc (V) | $\mathrm{Ta}=-40$ to $85^{\circ} \mathrm{C}$ |  | Unit | Test Conditions |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | Min | Max |  |  |
| Input voltage | $\mathrm{V}_{\mathrm{IH}}$ | 1.65 to 1.95 | $\mathrm{V}_{\mathrm{cc}} \times 0.65$ | - | V |  |
|  |  | 2.3 to 2.7 | 1.7 | - |  |  |
|  |  | 2.7 to 3.6 | 2.0 | - |  |  |
|  |  | 4.5 to 5.5 | $\mathrm{V}_{\mathrm{Cc} \times} \times 0.7$ | - |  |  |
|  | VIL | 1.65 to 1.95 | - | $\mathrm{V}_{\mathrm{Cc}} \times 0.35$ |  |  |
|  |  | 2.3 to 2.7 | - | 0.7 |  |  |
|  |  | 2.7 to 3.6 | - | 0.8 |  |  |
|  |  | 4.5 to 5.5 | - | $\mathrm{V}_{\mathrm{CC}} \times 0.3$ |  |  |
| Output voltage | $\mathrm{V}_{\mathrm{OH}}$ | 1.65 to 5.5 | $\mathrm{V}_{\mathrm{cc}}-0.2$ | - | V | $\mathrm{l} \mathrm{OH}=-100 \mu \mathrm{~A}$ |
|  |  | 1.65 | 1.2 | - |  | $\mathrm{loh}^{\prime}=-4 \mathrm{~mA}$ |
|  |  | 2.3 | 1.7 | - |  | $\mathrm{l}_{\mathrm{OH}}=-8 \mathrm{~mA}$ |
|  |  | 2.7 | 2.2 | - |  | $\mathrm{l}_{\mathrm{OH}}=-12 \mathrm{~mA}$ |
|  |  | 3.0 | 2.4 | - |  |  |
|  |  | 3.0 | 2.2 | - |  | $\mathrm{l}_{\mathrm{OH}}=-24 \mathrm{~mA}$ |
|  |  | 4.5 | 3.8 | - |  |  |
|  | VoL | 1.65 to 5.5 | - | 0.2 |  | $\mathrm{loL}=100 \mu \mathrm{~A}$ |
|  |  | 1.65 | - | 0.45 |  | $\mathrm{loL}=4 \mathrm{~mA}$ |
|  |  | 2.3 | - | 0.7 |  | $\mathrm{loL}=8 \mathrm{~mA}$ |
|  |  | 2.7 | - | 0.4 |  | $\mathrm{loL}=12 \mathrm{~mA}$ |
|  |  | 3.0 | - | 0.55 |  | $\mathrm{loL}=24 \mathrm{~mA}$ |
|  |  | 4.5 | - | 0.55 |  |  |
| Input current | $\mathrm{I}_{1}$ | 0 to 5.5 | - | $\pm 5.0$ | $\mu \mathrm{A}$ | $\mathrm{V}_{\mathrm{IN}}=5.5 \mathrm{~V}$ or GND |
| Output leak current | loff | 0 | - | $\pm 5.0$ | $\mu \mathrm{A}$ | $\mathrm{V}_{\text {IN }} / \mathrm{V}_{\text {OUt }}=5.5 \mathrm{~V}$ |
| Off state output current | loz | 2.7 to 5.5 | - | $\pm 5.0$ | $\mu \mathrm{A}$ | $\begin{aligned} & \mathrm{V}_{\text {IN }}=\mathrm{V}_{\mathrm{CC}} \text { or GND } \\ & \mathrm{V}_{\text {OUT }}=5.5 \mathrm{~V} \text { or GND } \end{aligned}$ |
| Quiescent supply current | Icc | 2.7 to 3.6 | - | $\pm 5.0$ | $\mu \mathrm{A}$ | $\mathrm{V}_{\mathrm{IN}}=3.6$ to 5.5 V |
|  |  | 2.7 to 5.5 | - | 5.0 | $\mu \mathrm{A}$ | $\mathrm{V}_{\text {IN }}=\mathrm{V}_{\text {cc }}$ or GND |
|  | $\Delta l_{\text {cc }}$ | 2.7 to 3.6 | - | 500 | $\mu \mathrm{A}$ | $\mathrm{V}_{\mathrm{IN}}=$ one input at $\left(\mathrm{V}_{\mathrm{CC}}-0.6\right) \mathrm{V}$, other inputs at $V_{C C}$ or GND |

## Switching Characteristics



Note: 1. This parameter is characterized but not tested.
tos $_{\text {LH }}=\mid$ tpLHm - t $_{\text {PLHn }} \mid$, tos $_{\text {HL }}=\mid$ t $_{\text {PHLm }}-\mathrm{t}_{\text {PHLL }} \mid$

## Operating Characteristics

| Item | Symbol | Vcc (V) | $\mathrm{Ta}=25^{\circ} \mathrm{C}$ |  |  | Unit | Test Conditions |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | Min | Typ | Max |  |  |
| Power dissipation capacitance | $\mathrm{C}_{\text {PD }}$ | 1.8 | - | 25 | - | pF | $\mathrm{f}=10 \mathrm{MHz}$ |
|  |  | 2.5 | - | 26 | - |  |  |
|  |  | 3.3 | - | 28 | - |  |  |
|  |  | 5.0 | - | 32 | - |  |  |

## Test Circuit



Note: 1. $C_{L}$ includes probe and jig capacitance.

## Waveforms - 1



Note: Input waveform: PRR = 10 MHz , duty cycle $50 \%$.

## Waveforms - 2



Note: Input waveform: PRR = 10 MHz , duty cycle $50 \%$.

## Waveforms - 3



| $\mathrm{V}_{\text {cc }}(\mathrm{V})$ | INPUT |  | Vref | $\mathrm{V}_{\text {TT }}$ | CL | RL | $\Delta \mathrm{V}$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\mathrm{V}_{\mathrm{IH}}$ | $\mathrm{t}_{\mathrm{r}} / \mathrm{t}_{\text {f }}$ |  |  |  |  |  |
| $\mathrm{V}_{\mathrm{CC}}=1.8 \pm 0.15 \mathrm{~V}$ | VCC | $\leq 2 \mathrm{~ns}$ | 1/2 V ${ }_{\text {cc }}$ | $2 \times \mathrm{Vcc}$ | 30 pF | $1.0 \mathrm{k} \Omega$ | 0.15 V |
| $\mathrm{V}_{\mathrm{CC}}=2.5 \pm 0.2 \mathrm{~V}$ | Vcc | $\leq 2 \mathrm{~ns}$ | $1 / 2 \mathrm{~V}_{\mathrm{CC}}$ | $2 \times V_{C C}$ | 30 pF | $500 \Omega$ | 0.15 V |
| $V_{C C}=2.7 \mathrm{~V}$ | 2.7 V | $\leq 2.5 \mathrm{~ns}$ | 1.5 V | 6 V | 50 pF | $500 \Omega$ | 0.3 V |
| $\mathrm{V}_{C C}=3.3 \pm 0.3 \mathrm{~V}$ | 2.7 V | $\leq 2.5 \mathrm{~ns}$ | 1.5 V | 6 V | 50 pF | $500 \Omega$ | 0.3 V |
| $\mathrm{V}_{C C}=5.0 \pm 0.5 \mathrm{~V}$ | Vcc | $\leq 2.5$ ns | $1 / 2 \mathrm{~V}$ cc | $2 \times \mathrm{V}$ cc | 50 pF | $500 \Omega$ | 0.3 V |

Notes: 1. Input waveform : PRR $=10 \mathrm{MHz}$, duty cycle $50 \%$.
2. Waveform - A shows input conditions such that the output is "L" level when enable by the output control.
3. Waveform - B shows input conditions such that the output is " H " level when enable by the output control.

## Package Dimensions




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