

# RD74LVC374B

## Octal D-type Flip Flops with 3-state Outputs

REJ03D0382-0100 Rev.1.00 Nov. 26, 2004

### **Description**

The RD74LVC374B has eight edge trigger D type flip flops with three state outputs in a 20 pin package. Data at the D inputs meeting set up requirements are transferred to the Q outputs on positive going transitions of the clock input. When the clock input goes low, data at the D inputs will be retained at the outputs until clock input returns high again. When a high logic level is applied to the output control input, all outputs go to a high impedance state, regardless of what signals are present at the other inputs and the state of the storage elements. Low voltage and high-speed operation is suitable at the battery drive product (note type personal computer) and low power consumption extends the life of a battery for long time operation.

### **Features**

- $V_{CC} = 1.65 \text{ V to } 5.5 \text{ V}$
- All inputs  $V_{IH}$  (Max.) = 5.5 V (@ $V_{CC}$  = 0 V to 5.5 V)
- All outputs  $V_{OUT}$  (Max.) = 5.5 V (@ $V_{CC}$  = 0 V or output off state)
- Typical  $V_{OL}$  ground bounce < 0.8 V (@ $V_{CC}$  = 3.3 V, Ta = 25 °C)
- Typical  $V_{OH}$  undershoot > 2.0 V (@ $V_{CC}$  = 3.3 V, Ta = 25°C)
- High output current  $\pm 4 \text{ mA} (@V_{CC} = 1.65 \text{ V})$

 $\pm 8 \text{ mA } (@V_{CC} = 2.3 \text{ V})$ 

 $\pm 12 \text{ mA } (@V_{CC} = 2.7 \text{ V})$ 

 $\pm 24 \text{ mA}$  (@V<sub>CC</sub> = 3.0 V to 5.5 V)

Ordering Information

Part Name	Package Type	Package Code	Package Abbreviation	Taping Abbreviation (Quantity)
RD74LVC374BFPEL	SOP-20 pin (JEITA)	FP-20DAV	FP	EL (2,000 pcs/reel)
RD74LVC374BTELL	TSSOP-20 pin	TTP-20DAV	Т	ELL (2,000 pcs/reel)

### **Function Table**

G	СК	D	Output Q
Н	X	X	Z
L	$\uparrow$	L	L
L	$\uparrow$	Н	Н
L	L	X	$Q_0$

H: High level

L: Low level

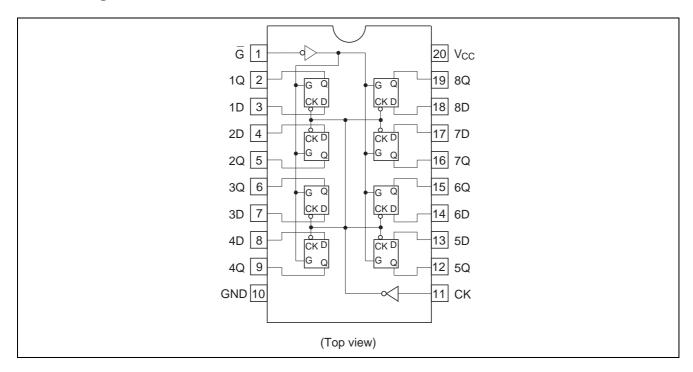
X: Immaterial

Z: High impedance

↑: Low to high transition

Q<sub>0</sub>: Level of Q before the indicated steady input conditions were established.

## **Pin Arrangement**



## **Absolute Maximum Ratings**

Item	Symbol	Ratings	Unit	Conditions	
Supply voltage	V <sub>CC</sub>	-0.5 to 7.0	V		
Input diode current	I <sub>IK</sub>	-50	mA	V <sub>I</sub> = -0.5 V	
Input voltage	Vı	-0.5 to 7.0	V		
Output diode current	I <sub>OK</sub>	-50	mA	V <sub>O</sub> = -0.5 V	
		50		$V_{O} = V_{CC} + 0.5 \text{ V}$	
Output voltage	Vo	-0.5 to V <sub>CC</sub> +0.5	V	Output "H" or "L"	
		-0.5 to 7.0		Output "Z" or V <sub>CC</sub> :OFF	
Output current	I <sub>O</sub>	±50	mA		
V <sub>CC</sub> , GND current / pin	I <sub>CC</sub> or I <sub>GND</sub>	100	mA		
Storage temperature	Tstg	-65 to +150	°C		

Note: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

## **Recommended Operating Conditions**

Item	Symbol	Ratings	Unit	Conditions
Supply voltage	V <sub>CC</sub>	1.5 to 5.5	V	Data hold
		1.65 to 5.5		At operation
Input / output voltage	VI	0 to 5.5	V	G, CK, D
	Vo	0 to V <sub>CC</sub>		Output "H" or "L"
		0 to 5.5		Output "Z" or V <sub>CC</sub> :OFF
Operating temperature	Та	-40 to 85	°C	
Output current	I <sub>OH</sub>	-4	mA	V <sub>CC</sub> = 1.65 V
		-8		V <sub>CC</sub> = 2.3 V
		-12		V <sub>CC</sub> = 2.7 V
		-24		$V_{CC} = 3.0 \text{ V to } 5.5 \text{ V}$
	I <sub>OL</sub>	4	mA	V <sub>CC</sub> = 1.65 V
		8		V <sub>CC</sub> = 2.3 V
		12		V <sub>CC</sub> = 2.7 V
		24		V <sub>CC</sub> = 3.0 V to 5.5 V
Input rise / fall time *1	t <sub>r</sub> , t <sub>f</sub>	20	ns/V	V <sub>CC</sub> = 1.65 V to 2.7 V
		10		V <sub>CC</sub> = 3.0 V to 5.5 V

Notes: 1. This item guarantees maximum limit when one input switches.

Waveform: Refer to test circuit of switching characteristics.

## **Electrical Characteristics**

	Symbol		Ta = -4	Ta = -40 to 85°C		
Item		V <sub>cc</sub> (V)	Min	Min Max		Test Conditions
Input voltage	V <sub>IH</sub>	1.65 to 1.95	V <sub>CC</sub> ×0.65	_	V	
		2.3 to 2.7	1.7	_		
		2.7 to 3.6	2.0	_		
		4.5 to 5.5	V <sub>CC</sub> ×0.7	_		
	V <sub>IL</sub>	1.65 to 1.95	_	V <sub>CC</sub> ×0.35		
		2.3 to 2.7	_	0.7		
		2.7 to 3.6	_	0.8		
		4.5 to 5.5	_	V <sub>CC</sub> ×0.3		
Output voltage	V <sub>OH</sub>	1.65 to 5.5	V <sub>CC</sub> -0.2	_	V	I <sub>OH</sub> = -100 μA
		1.65	1.2	_		I <sub>OH</sub> = -4 mA
		2.3	1.7	_		$I_{OH} = -8 \text{ mA}$
		2.7	2.2	_		$I_{OH} = -12 \text{ mA}$
		3.0	2.4	_		
		3.0	2.2	_		$I_{OH} = -24 \text{ mA}$
		4.5	3.8	_		
	V <sub>OL</sub>	1.65 to 5.5	_	0.2		$I_{OL} = 100 \mu A$
		1.65	_	0.45		I <sub>OL</sub> = 4 mA
		2.3	_	0.7		$I_{OL} = 8 \text{ mA}$
		2.7	_	0.4		I <sub>OL</sub> = 12 mA
		3.0	_	0.55		I <sub>OL</sub> = 24 mA
		4.5	_	0.55		
Input current	I <sub>IN</sub>	0 to 5.5	_	±5.0	μΑ	$V_{IN} = 5.5 \text{ V or GND}$
Output leak current	I <sub>OFF</sub>	0	_	±5.0	μΑ	V <sub>IN</sub> / V <sub>OUT</sub> = 5.5 V
Off state output current	loz	2.7 to 5.5		±5.0	μΑ	V <sub>IN</sub> = V <sub>CC</sub> or GND
						V <sub>OUT</sub> = 5.5 V or GND
Quiescent supply	I <sub>CC</sub>	2.7 to 3.6	_	±5.0	μΑ	V <sub>IN</sub> = 3.6 to 5.5 V
current		2.7 to 5.5	_	5.0	μΑ	$V_{IN} = V_{CC}$ or GND
	$\Delta I_{CC}$	2.7 to 3.6		500	μΑ	$V_{IN}$ = one input at( $V_{CC}$ -0.6)V, other inputs at $V_{CC}$ or GND

## **Switching Characteristics**

Item	Symbol	V <sub>cc</sub> (V)	1	Ta = -40 to 85°C			From	То
			Min	Тур	Max	Unit	(Input)	(Output)
Maximum clock	f <sub>max</sub>	1.8±0.15	_	_	55.0	MHz		
frequency		2.5±0.2	_	_	95.0			
		2.7	_	_	150.0			
		3.3±0.3	_	_	150.0			
		5.0±0.5	_	_	150.0			
Propagation delay time	t <sub>PLH</sub>	1.8±0.15	1.0	_	21.6	ns	СК	Q
	t <sub>PHL</sub>	2.5±0.2	1.0	_	10.5			
		2.7	1.0	_	8.0			
		3.3±0.3	2.2	_	7.0			
		5.0±0.5	1.0	_	5.5			
Output enable time	$t_{ZH}$	1.8±0.15	1.0	_	19.5	ns	G	Q
	$t_{ZL}$	2.5±0.2	1.0	_	10.5			
		2.7	1.0	_	8.5			
		3.3±0.3	1.5	_	7.5			
		5.0±0.5	1.0	_	5.5			
Output disable time	t <sub>HZ</sub>	1.8±0.15	1.0	_	18.8	ns	G	Q
·	$t_{LZ}$	2.5±0.2	1.0	_	7.8			
		2.7	1.0	_	7.0			
		3.3±0.3	1.7	_	6.4			
		5.0±0.5	1.0	_	5.4			
Setup time	t <sub>su</sub>	1.8±0.15	6.0	_	_	ns		
·		2.5±0.2	4.0	_	_			
		2.7	2.0	_	_			
		3.3±0.3	2.0	_	_			
		5.0±0.5	2.0	_	_			
Hold time	t <sub>h</sub>	1.8±0.15	4.0	_	_	ns		
		2.5±0.2	2.0	_	_			
		2.7	1.5	_	_			
		3.3±0.3	1.5	_	_			
		5.0±0.5	1.5	_	_			
Pulse width	t <sub>w</sub>	1.8±0.15	9.0	_	_	ns		
		2.5±0.2	4.0	_	_			
		2.7	3.3	_	_			
		3.3±0.3	3.3	_	_			
		5.0±0.5	3.3	_	_			
Between output	toslh	1.8±0.15	_	_	_	ns		
pins skew <sup>*1</sup>	toshl	2.5±0.2	_	_	_	7		
		2.7	_	_	_	7		
		3.3±0.3	_	_	1.0	7		
		5.0±0.5	_	_	1.0	7		
Input capacitance	C <sub>IN</sub>	3.3	_	4.0	_	pF		
Output capacitance	Co	3.3	_	8.0	_	pF	1	1

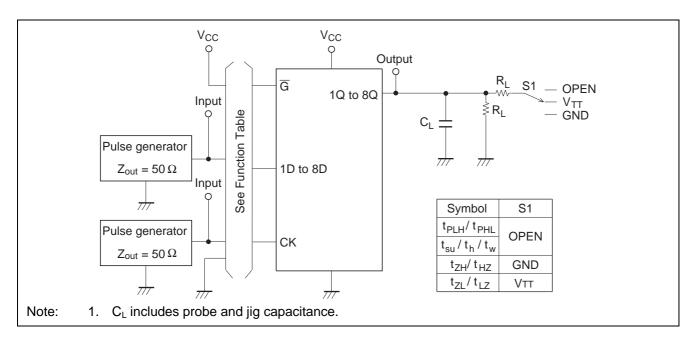
Note: 1. This parameter is characterized but not tested.

 $tos_{LH} = |\ t_{PLHm} - t_{PLHn}|,\ tos_{HL} = |\ t_{PHLm} - t_{PHLn}|$ 

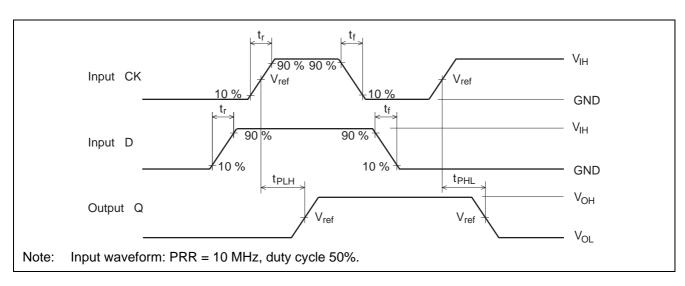
## **Operating Characteristics**

			Ta = 25°C				
Item	Symbol	V <sub>CC</sub> (V)	Min	Тур	Max	Unit	Test Conditions
Power dissipation	C <sub>PD</sub>	1.8	_	25	_	pF	f = 10 MHz
capacitance		2.5	_	26	_		
		3.3	_	28	_		
		5.0	_	32	_		

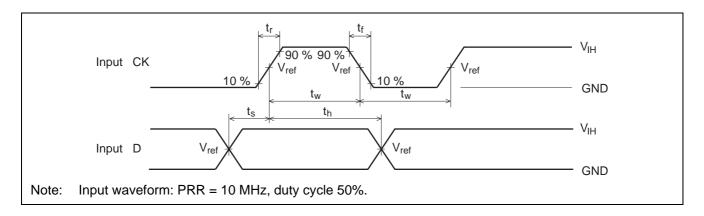
### **Test Circuit**



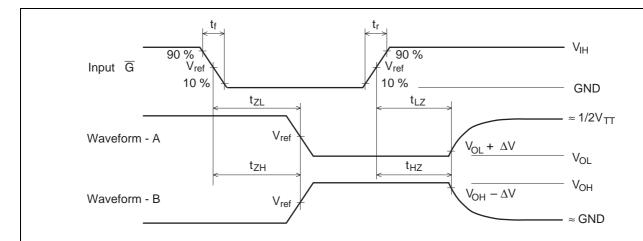
### Waveforms - 1



### Waveforms - 2



### Waveforms - 3

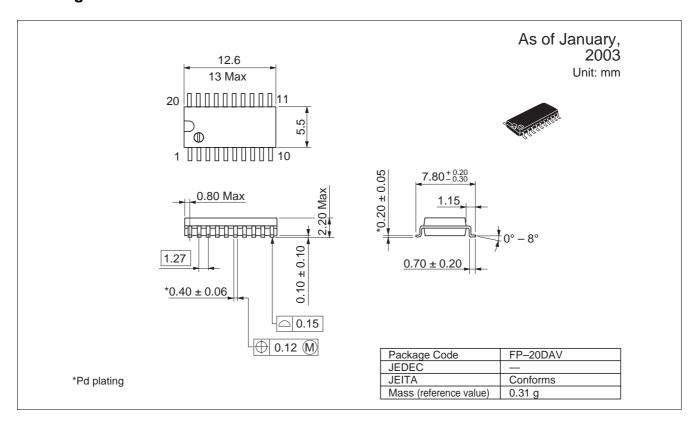


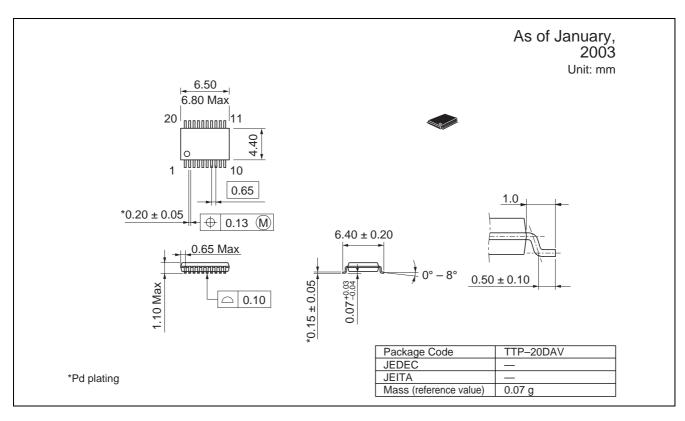
	INPUTS						
V <sub>CC</sub> (V)	V <sub>IH</sub>	t <sub>r</sub> /t <sub>f</sub>	Vref	V <sub>TT</sub>	CL	$R_L$	ΔV
$V_{CC} = 1.8 \pm 0.15 \text{ V}$	Vcc	≤ 2 ns	1/2 V <sub>CC</sub>	2× V <sub>CC</sub>	30 pF	1.0 kΩ	0.15 V
$V_{CC} = 2.5 \pm 0.2 \text{ V}$	Vcc	≤ 2 ns	1/2 V <sub>CC</sub>	2× V <sub>CC</sub>	30 pF	500 Ω	0.15 V
$V_{CC} = 2.7 V$	2.7 V	≤ 2.5 ns	1.5 V	6 V	50 pF	500 Ω	0.3 V
$V_{CC} = 3.3 \pm 0.3 \text{ V}$	2.7 V	≤ 2.5 ns	1.5 V	6 V	50 pF	500 Ω	0.3 V
$V_{CC} = 5.0 \pm 0.5 \text{ V}$	Vcc	≤ 2.5 ns	1/2 V <sub>CC</sub>	2× V <sub>CC</sub>	50 pF	500 Ω	0.3 V

Notes:

- 1. Input waveform: PRR = 10 MHz, duty cycle 50%.
- 2. Waveform A shows input conditions such that the output is "L" level when enable by the output control.
- 3. Waveform B shows input conditions such that the output is "H" level when enable by the output control.

## **Package Dimensions**





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